Claims

[c1]   The method of modeling the junction capacitance of the MOSFET and a analysis technique of it for high-speed circuit simulator comprising that a) the way to input a net-list of the circuit to be simulated b)creation procedure to make template net-list as the input net-list for external simulator c)creation procedure to make a lookup table model for the MOSFET d)how to model the value of the junction capacitance of MOSFET as piecewise constant